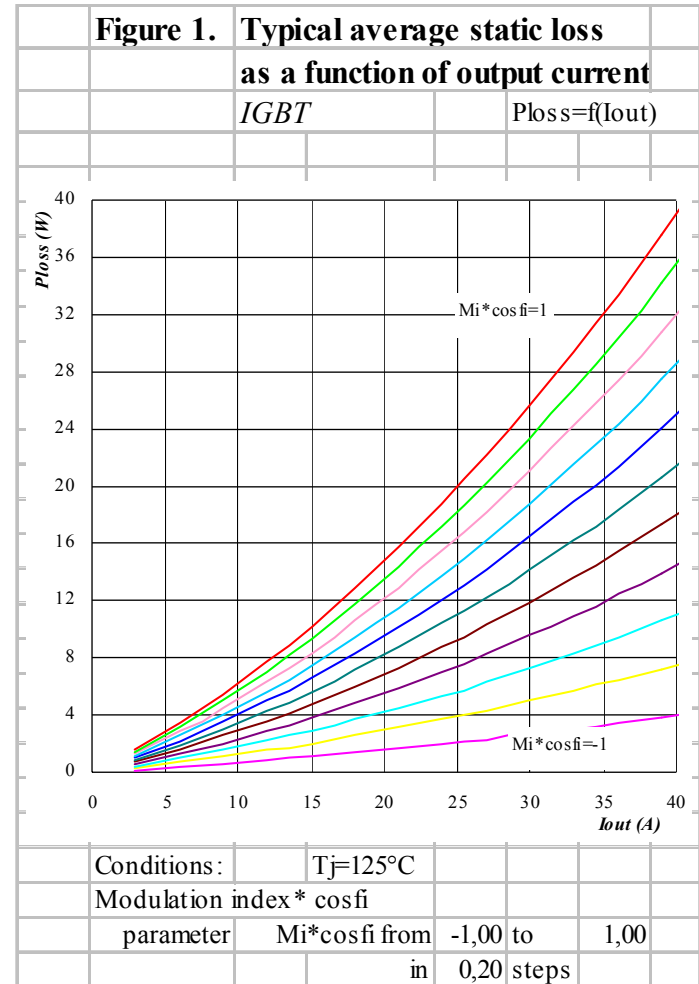

Interpretation of Curves for Motor Drive Applications



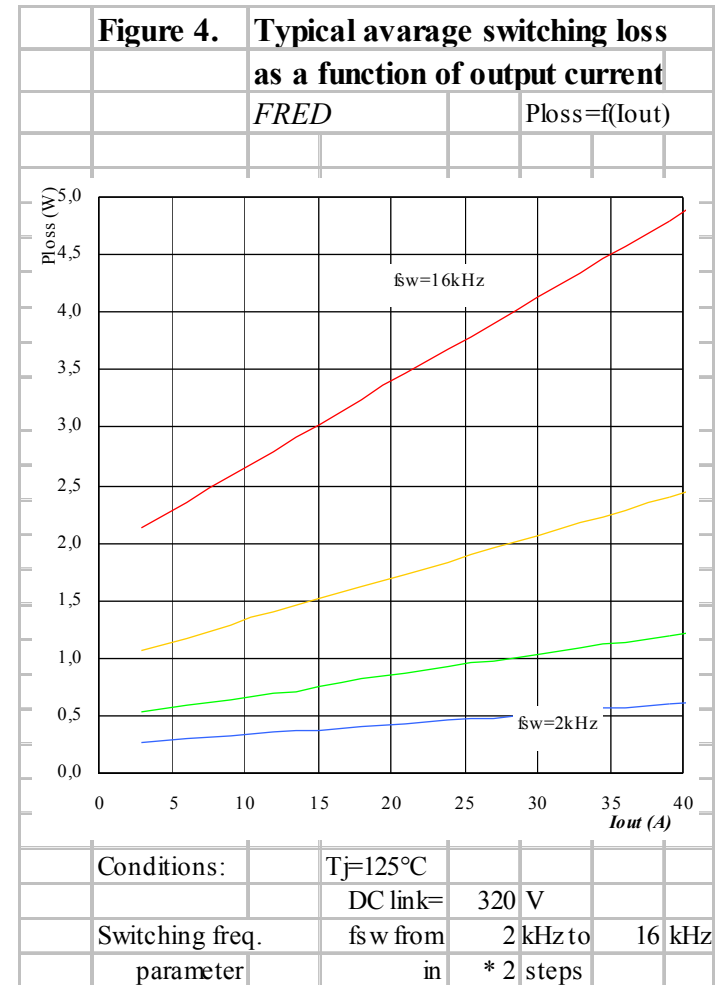
Typical average static loss as a function of output current

- These diagrams give estimation for the IGBT and FRED static losses as a function of output RMS phase current
- The curves are given with different $M_i \cdot \cos \phi_i$ parameters
- M_i defines the ratio of phase peak output voltage to half of DC link voltage
- M_i is positive for motoring and negative for braking mode
- In motoring mode the IGBT have high dissipation the FRED have low dissipation
- In braking mode the IGBT have low dissipation the FRED have high dissipation
- $\cos \phi_i$ is the cosines of delay of phase current to phase output voltage
- The diagram gives average losses that might have high swings at low FC frequencies



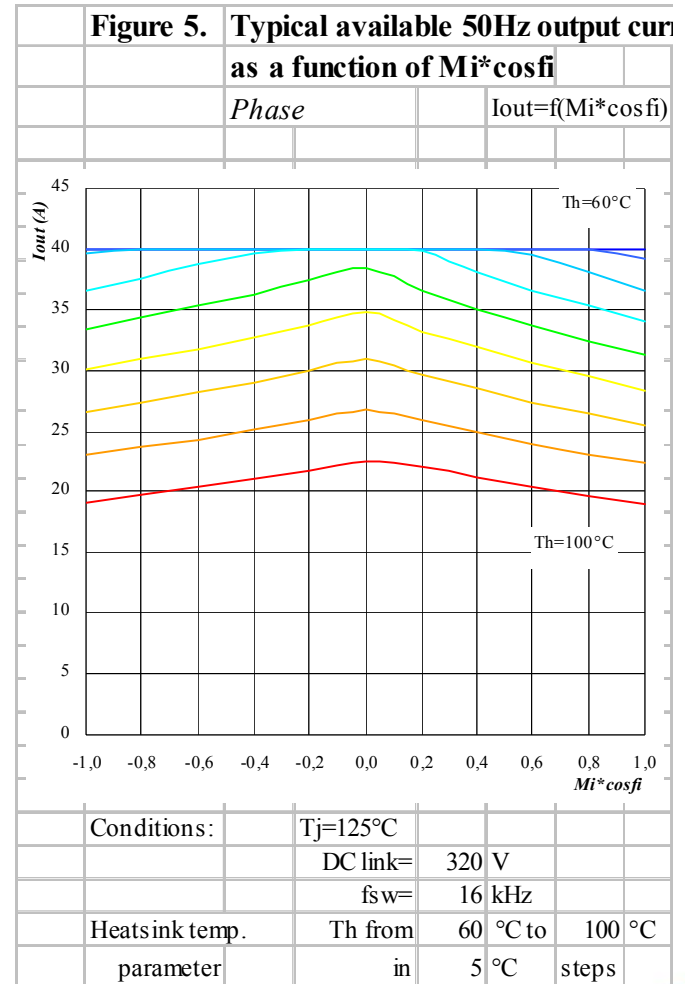
Typical average switching loss as a function of output current

- These diagrams give estimation for the IGBT and FRED switching losses as a function of output RMS phase current
- The curves are given with different f_{sw} switching frequency parameters
- The switching losses are always linear with f_{sw} but more curves are given for easy reading
- The losses at low currents indicate the capacitive switching losses
- The losses are proportional with DC link voltage for smaller variations
- The diagram gives average losses that might have high swings at low FC frequencies



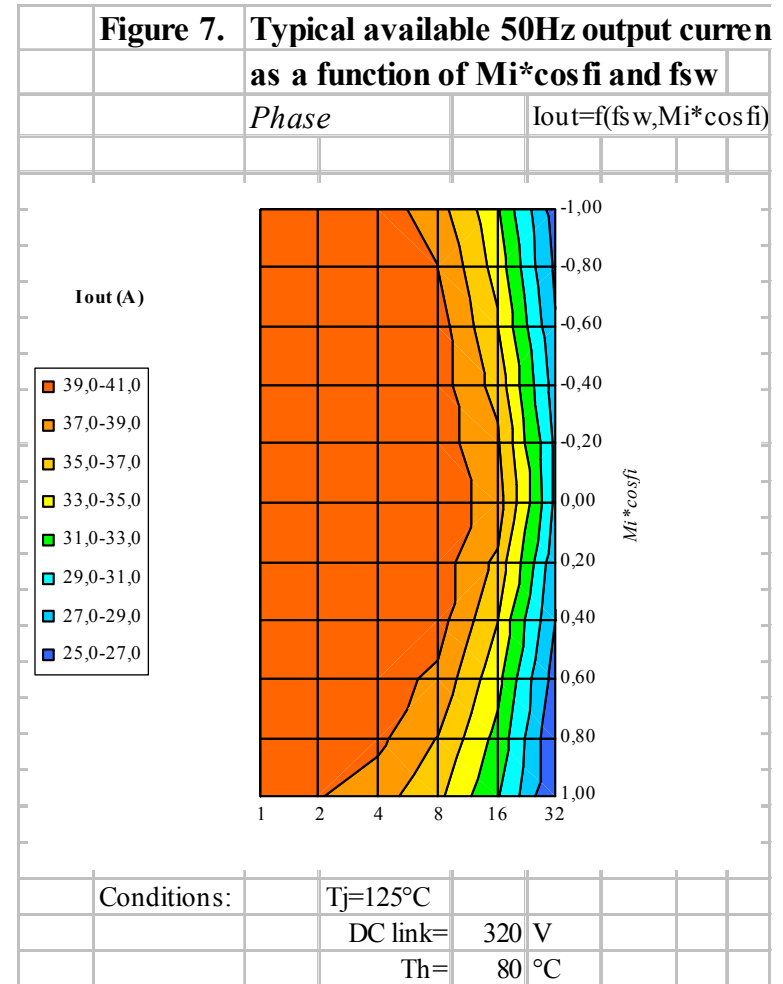
Typical available 50Hz output current as a function of $Mi \cdot \cos\phi_i$

- This diagram gives estimation for the value of output RMS phase current as a function of $Mi \cdot \cos\phi_i$
- The curves are given with different T_h heat sink temperature parameters.
- Heat sink temperature is to be interpreted heat sink temperature just below thermal grease
- The lower the heat sink temperature the higher the available current but at low heat sink temperatures the current is limited by wire bonding
- Typically the positive $Mi \cdot \cos\phi_i$ motoring range is limited by the IGBT while the negative braking range by the FRED
- The component sizing is best if the curves are symmetrical to $Mi \cdot \cos\phi_i = 0$
- The f_{sw} switching frequency is fixed typically to 16kHz for 600V and to 4kHz for 1200V parts



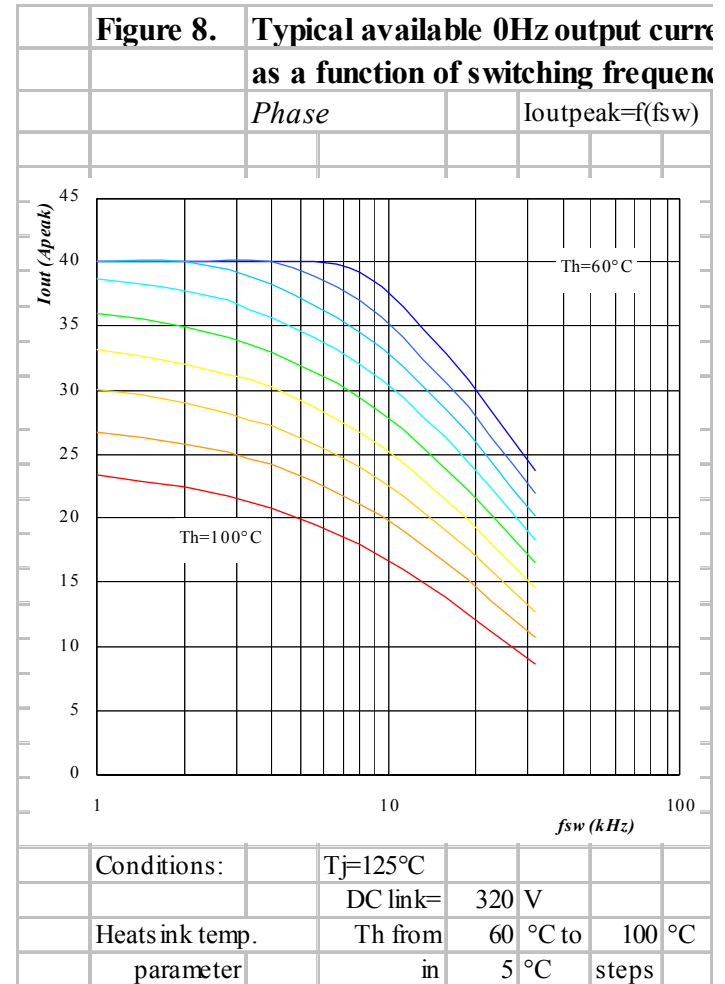
Typical available 50Hz output current as a function of $M_i \cdot \cos \phi_i$ and f_{sw}

- This surface diagram gives help for the selection of the operating parameters to meet the output RMS phase current requirement
- The surfaces are given at fixed $T_h = 80^\circ\text{C}$ heat sink temperature



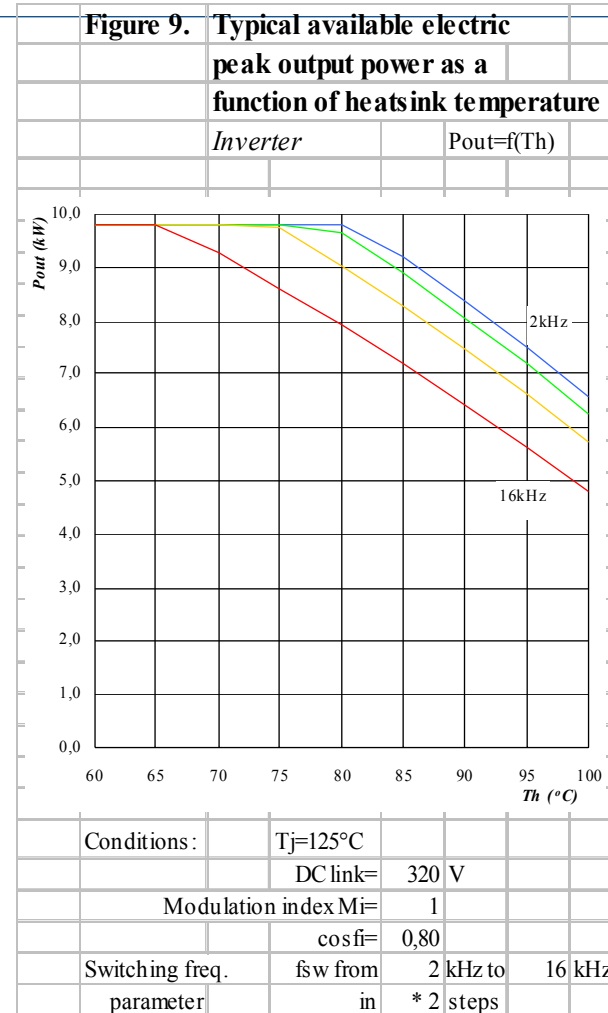
Typical available 0Hz output current as a function of switching frequency

- This diagram gives estimation for the value of output phase current peak as a function of fsw switching frequency at 0Hz of FC
- This operation mode of FC is entered when starting a motor from still position or applying DC braking
- It is important to note that the available 0Hz current value can be much less than at 50Hz
- Some of the IGBTs and some of the FREDs are under DC switching conditions that is DC current chopped to about 50% duty cycle at fsw switching frequency



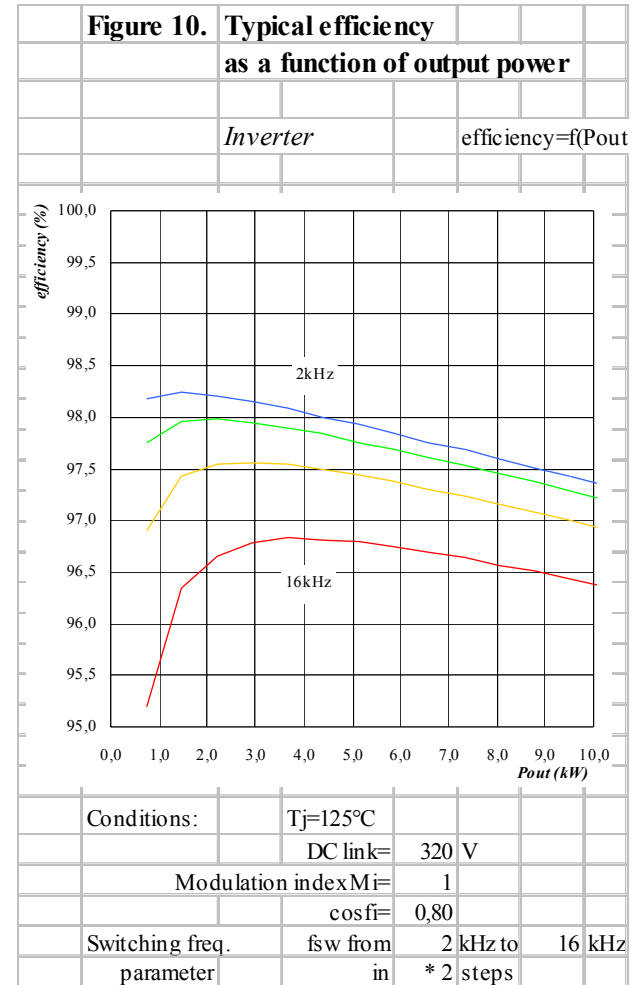
Typical available electric peak output power as a function of heat sink temperature

- This diagram gives estimation for the value of output 3 phase output power calculated from 50Hz RMS current
- The power is calculated as real and undistorted that is taking into account the given cosfi and leaving a 10% spare space for voltage drop from supply. The drop is for covering DC voltage ripple and switch efficiency
- The modulation index $M_i=1$ condition includes that the PWM has built in compensation for DC link ripple for maximum undistorted output voltage
- The power at low heat sink temperatures can be limited by current capability of wire bonds



Typical efficiency as a function of output power

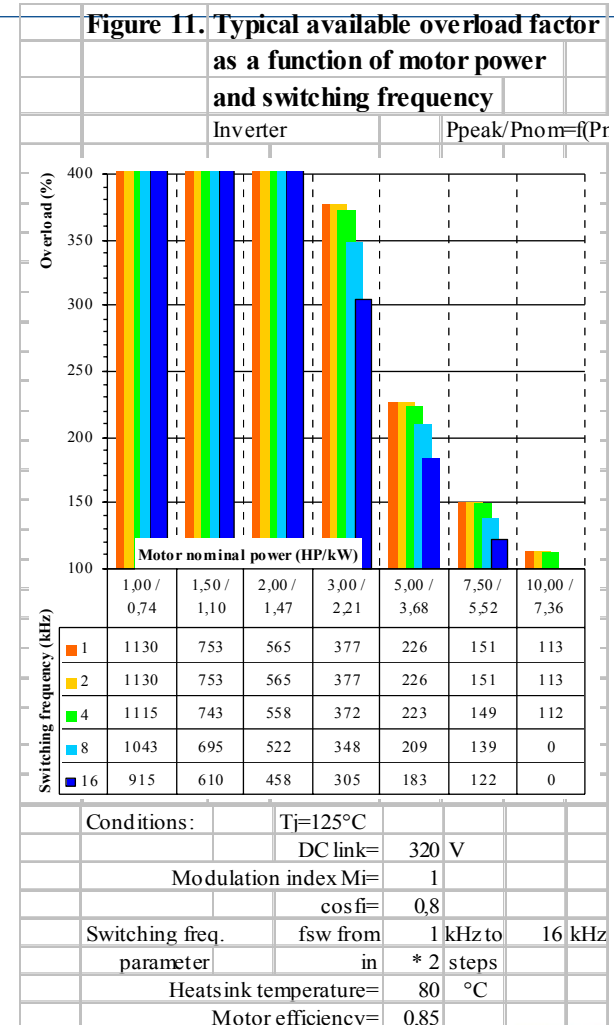
- The diagram is an estimation for the efficiency of the inverter with switches working under worst $T_j=125^\circ\text{C}$ conditions.
- If the switches are operated at lower T_j junction temperature the efficiency is higher due to the positive temperature coefficient of static and switching losses
- The degradation of efficiency at low loads is a consequence of capacitive switching losses



Typical available overload factor as a function of motor power and switching frequency

- The overload factor is calculated as typical electrical output power relative to the electrical power requirement of standard sized motors with 0,85% motor efficiency.
- The standard ratings of motors are mechanical power at rated voltage and RPM
- When the inverter uses special modulation techniques like 3th harmonic or flat-top instead of the simple SPWM to get 10-15% output voltage increase then output power and overload factor can also be higher.

All fixed parameters of the calculations can be changed to immediately get diagrams with specific conditions



Selection Guide

- Diagrams of the selection guide are a subset of the diagrams of the application guides to compare the available power graphs within a family
- The power surface plots make the selection easy for a given electric power level at known heating and switching frequency conditions
- The overload factor comparison charts help the selection if the motor to be driven and the required overload factor are both known at standard conditions

